

1971 U. S. PRO
09/965509
09/26/01

Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

BEST AVAILABLE COPY

U.S. UTILITY Patent Application

 O.I.P.E. SCANNED <u>15-3</u> Q.A. <u>Am</u>	PATENT DATE
---	--------------------

APPLICATION NO. 09/965509	CONT/PRIOR D	CLASS 438	SUBCLASS 234	ART UNIT 2812 2818	EXAMINER R. BERRY
------------------------------	-----------------	--------------	-----------------	--------------------------	----------------------

APPUCANTS

Dem Basceri
Mark Visokay
Thomas Graettinger
Steven Cummings

title

Methods for forming and integrated circuit structures containing enhanced-surface-area conductive layers

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	

WARNING:

WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-438A
(Rev. 6/99)

FILED WITH: DISK (CRF) FICHE CD-ROM
(Attached in pocket on right inside flap)